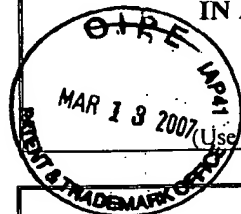


**March 9, 2007**

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**FIRST NAMED INVENTOR**  
Richard M. Wyatt

**EXAMINER**  
Robert W. Wilson

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## /RW/

AR

Yamanaka, H., *et al.*, "Scalable Shared-Buffering ATM Switch with a Versatile Searchable Queue," *IEEE Journal on Selected Areas in Communications* 15(5): pp. 773-784 (June 1997).

AS

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AT

Xiong, Y., *et al.*, "Analysis of Multicast ATM Switching Networks Using CRWR Scheme," *Computer Networks and ISDN Systems* 30(8): pp. 835-854 (May 1998).

/Robert Wilson/

DATE CONSIDERED

04/26/2007

IFW

26168  
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DOCKET NO.: 2037.2002-000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Richard M. Wyatt

Application No.: 09/559,190

Group: 2616

Filed: April 27, 2000

Examiner: Robert W. Wilson

Confirmation No.: 2260

For: PORT PACKET QUEUING

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- ☐ under 37 CFR 1.97(b), or  
(Within any one of the following time periods: three months of filing national application (other than a CPA) or date of entry of the national stage in an international application; or before the mailing date of a first office action on the merits in a non-provisional application, including a CPA, or a Request for Continued Examination).
- ☐ under 37 CFR 1.97(c) together with either:  
☐ a Statement under 37 CFR 1.97(e), as checked below, or  
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